

CAEN India Academy Tour India 2026

Report of Contributions

Contribution ID: 1

Type: **not specified**

Introduction to CAEN 2nd Generation Digitizers

Saturday 11 April 2026 10:00 (45 minutes)

Comprehensive overview of the x2740, x2745, x2730, and x2751 series.

Topics include architecture, key features, real-time digital signal processing capabilities, application examples in nuclear and scientific instrumentation, and integration within modern DAQ systems.

Presenter: Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 2

Type: **not specified**

Sci-Compiler Framework Presentation

Saturday 11 April 2026 10:45 (45 minutes)

Introduction to the Sci-Compiler graphical FPGA development environment: block-diagram firmware design, automated VHDL generation, deployment on CAEN digitizers, and integration with SciSDK (Python/C++) for control and readout.

Presenter: ABBA, Andrea

Contribution ID: 3

Type: **not specified**

Q&A Session and Open Discussion

Contribution ID: 4

Type: **not specified**

Hands-On Session

Saturday 11 April 2026 11:45 (1h 15m)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 5

Type: **not specified**

Hands-On Session

Saturday 11 April 2026 14:00 (2 hours)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 6

Type: **not specified**

Introduction to CAEN 2nd Generation Digitizers

Monday 13 April 2026 10:00 (45 minutes)

Comprehensive overview of the x2740, x2745, x2730, and x2751 series.

Topics include architecture, key features, real-time digital signal processing capabilities, application examples in nuclear and scientific instrumentation, and integration within modern DAQ systems.

Presenter: Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 7

Type: **not specified**

Introduction to CAEN 2nd Generation Digitizers

Thursday 16 April 2026 10:00 (45 minutes)

Comprehensive overview of the x2740, x2745, x2730, and x2751 series.

Topics include architecture, key features, real-time digital signal processing capabilities, application examples in nuclear and scientific instrumentation, and integration within modern DAQ systems.

Presenter: Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 8

Type: **not specified**

Sci-Compiler Framework Presentation

Thursday 16 April 2026 10:45 (45 minutes)

Introduction to the Sci-Compiler graphical FPGA development environment: block-diagram firmware design, automated VHDL generation, deployment on CAEN digitizers, and integration with SciSDK (Python/C++) for control and readout.

Presenter: ABBA, Andrea

Contribution ID: 9

Type: **not specified**

Sci-Compiler Framework Presentation

Monday 13 April 2026 10:45 (45 minutes)

Introduction to the Sci-Compiler graphical FPGA development environment: block-diagram firmware design, automated VHDL generation, deployment on CAEN digitizers, and integration with SciSDK (Python/C++) for control and readout.

Presenter: ABBA, Andrea

Contribution ID: **10**

Type: **not specified**

Hands-On Session

Monday 13 April 2026 11:45 (1h 15m)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: **11**

Type: **not specified**

Hands-On Session

Thursday 16 April 2026 11:45 (1h 15m)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 12

Type: **not specified**

Hands-On Session

Monday 13 April 2026 14:00 (2 hours)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)

Contribution ID: 13

Type: **not specified**

Hands-On Session

Thursday 16 April 2026 14:00 (2 hours)

Presenters: ABBA, Andrea; Dr VENARUZZO, Massimo (CAEN s.p.a.)